



Patent
Attorney's Docket No. 027260-481

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Patent Application of)
Michio KOMODA) Group Art Unit: 2812
Application No.: 09/921,604) Examiner: Unassigned
Filed: August 6, 2001)
For: CIRCUIT MODIFICATION METHOD)
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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant hereby submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

- 1) Japanese Patent No. 2000-137554, dated May 16, 2000;
- 2) Japanese Patent No. 8-102491, dated April 16, 1996; and
- 3) R. Levy, et al., "ClariNet: A noise analysis tool for deep submicron design", p.233,
(http://www.sigda.acm.org/Archives/ProceedingArchives/Dac/Dac2000/papers/2000/dac00/htmlfiles/sun_sgi/dacabs.htm#14_1).

Reference 2 is cited on page 2 of the Specification.

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

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